

Department: CE, EEE, ME, ECE, CSE & IT

Academic year: 2023-2024


Class : B.Tech. - 3rd Semester - **AR20** - Supplementary Practical Exams, December 2023

TIME TABLE FOR PRACTICAL EXAMS

BRANCH	LABORATORY NAME & CODE	DATE	TIME	JNTU Nos.
EEE	Semiconductor Devices and Circuits, (20EE306)	21/12/2023	09.00 AM TO 12.00 Noon	20341A0256, 257
ME	Manufacturing Technology (20ME301)	18/12/2023	09.00 AM TO 12.00 Noon	20341A0364, 393
ECE	Signals & Systems (20EC305)	20/12/2023	02.00 PM To 5.00 PM	20341A0405, 418, 444, 4D1, 4G8, 4H7
	Linear Circuit Analysis (20EC302)	21/12/2023	02.00 PM To 5.00 PM	20341A0432, 4D1 & 21345A0417
CSE	Digital Logic Design (20CS304)	21/12/2023	09.00 AM TO 12.00 Noon	20341A05D9
IT	Digital Logic Design (20CS304)	21/12/2023	09.00 AM TO 12.00 Noon	20341A12A8

Note: Any discrepancies or clashes may be brought to the notice of the Examination section on or before 15-12-2023


ACE


Controller of Examinations